



**NOTES:**

- VOLTAGES VARY WITH ASSOCIATED CONTROL SETTING. LINE VOLTAGE MAINTAINED AT 120V AC.
- ALL VOLTAGES + 10% AS MEASURED FROM POINT INDICATED TO CHASSIS USING A HIGH IMPEDANCE VTVM, NO SIGNAL INPUT. CONTRAST & BRIGHTNESS CONTROLS AT MAXIMUM. ALL OTHER CONTROLS IN NORMAL OPERATING POSITION.

**WAVEFORM MEASUREMENTS**

- TAKEN FROM POINT INDICATED TO CHASSIS WITH A WIDE-BAND OSCILLOSCOPE SYNCED NEAR SWEEP RATE INDICATED. INPUT SIGNAL 5V PP, CONTRAST & BRIGHTNESS CONTROLS AT MAXIMUM. ALL OTHER CONTROLS IN NORMAL OPERATING POSITION.

INDICATES PRINTED CIRCUIT BOARDS.

UNLESS OTHERWISE SPECIFIED:  
CAPACITOR DECIMAL VALUE IN MF. ALL OTHERS PF. CAPACITANCE VALUES ONLY ARE SHOWN ON SCHEMATIC DIAGRAM. FOR COMPLETE DESCRIPTION REFER TO PARTS LIST.  
RESISTORS ARE 10%, 1/2W.

