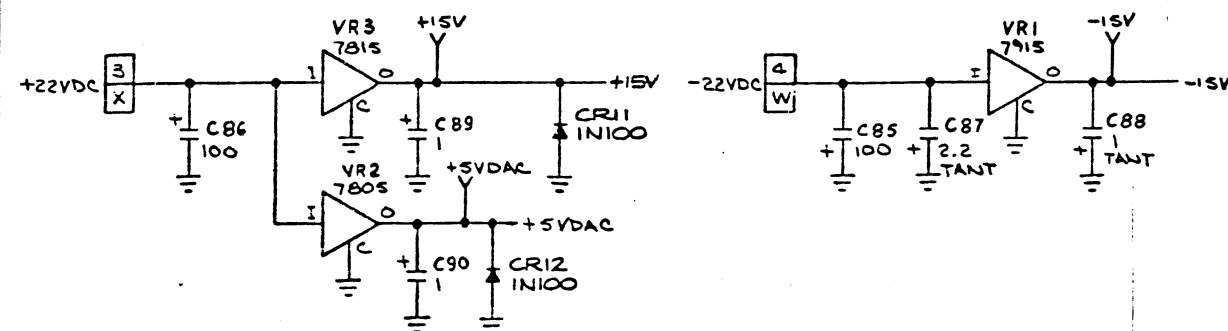
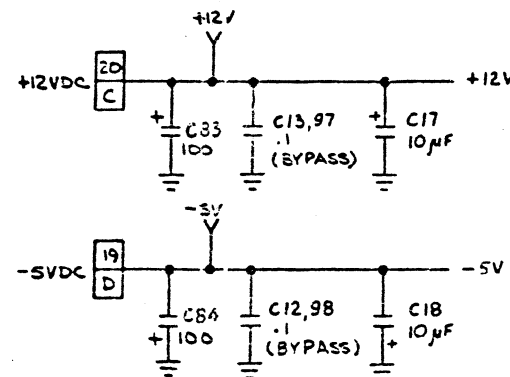
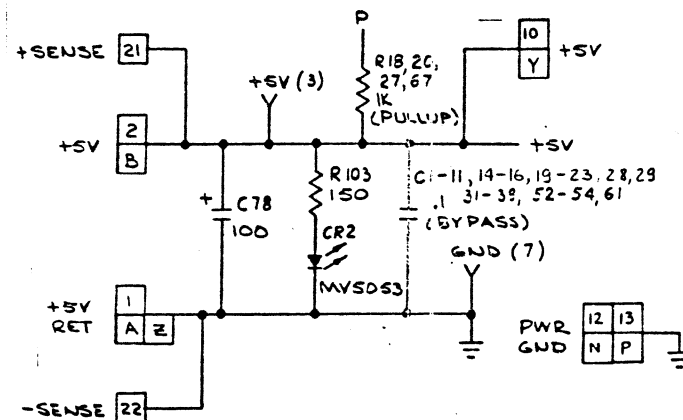


8		7		6				
IC CHART								
TYPE	VOLTAGE / PIN NO.							UNUSED GATES
	GND	+5V	+5V DAC	+12V	+15V	-5V	-5V	
74S00	7	14						
74LS00	7	14						
74S02	7	14						
74LS02	7	14						
74S04	7	14						P5, P5, P5,
74LS04	7	14						
74LS06	7	14						
74LS08	7	14						
74LS12	7	14						K4
74LS11	7	14						
74LS14	7	14						
74LS22	7	14						M5
74LS27	7	14						
74LS32	7	14						L2
74LS42	8	16						
74S74	7	14						
74LS74	7	14						
74S86	7	14						B7
74LS86	7	14						
74LS129	8	16						
74LS139	8	16						
74LS157	8	16						
74LS161	8	16						
74LS164	7	14						
74LS174	8	16						
74LS175	8	16						
74LS191	8	16						
74193	8	16						
74LS193	8	16						
74LS194	8	16						
74LS244	10	20						
74LS245	10	20						
74S260	7	14						
74LS273	10	20						
74LS367	8	16						
74LS393	7	14						
74LS670	8	16						
2114 (RAM)	9	18						
8316E (ROM)	12	24, 24		19				
2708 (ROM)	12	24					21	
82S129 (PROM)	8	16						
6502A (MICROPROCESSOR)	1, 21	8						
AM6012	13		20			17		
MC3410	2		14			1		
DAC-08			13			3		
LM319	3, 2				11	6		
TLO82					8	4		
TLO84					4	11		
LF13201	5				13	4		
14050	8	1						
7404	7	14						H11, H11, H11, H11
74LS399	8	16						



MEMORY MAP										
ADDRESS		DATA								FUNCTION
0000-00FF	X 0 0 0 0	X 1 A A A A A A A A A A	X 0 D 0 D 0 D 0 D 0 D	PROGRAM RAM (1K)						
0030	X 0 0 0 1	0 0 X X X X X X X X	X 1 X 1 X 1 X 1 X 1 X 1	RIGHT COIN						
			X 1 X 1 X 1 X 1 X 1 X 1	CENTER COIN						
			X 1 X 1 X 1 X 1 X 1 X 1	LEFT COIN						
			X 1 X 1 X 1 X 1 X 1 X 1	SLAM SW						
			X 1 X 1 X 1 X 1 X 1 X 1	SELF TEST SW						
			X 1 X 1 X 1 X 1 X 1 X 1	DIAG TEST SW						
			X 1 X 1 X 1 X 1 X 1 X 1	HALT						
			X 1 X 1 X 1 X 1 X 1 X 1	3KHz						
0A30	X 0 0 0	0 1 X X X X X X X X	X 0 0 0 0 0 0 0 0	OPTIONS 0						
0C00	X 0 0 0	0 1 X X X X X X X X	X 0 0 0 0 0 0 0 0	OPTIONS 1						
1000	X 0 0 1	0 0 X X X X X X X X	X 1 X 1 X 1 X 1 X 1 X 1	COIN COUNTER RIGHT						
			X 1 X 1 X 1 X 1 X 1 X 1	COIN COUNTER CENTER						
			X 1 X 1 X 1 X 1 X 1 X 1	COIN COUNTER LEFT						
			X 1 X 1 X 1 X 1 X 1 X 1	VIDEO INVERT X						
			X 1 X 1 X 1 X 1 X 1 X 1	VIDEO INVERT Y						
1200	X 0 0 0	0 1 X X X X X X X X	X 1 X 1 X 1 X 1 X 1 X 1	VECTOR GENERATOR G0						
1C00	X 0 0 0	0 1 X X X X X X X X	X 1 X 1 X 1 X 1 X 1 X 1	WATCHDOG CLEAR						
1E00	X 0 0 0	0 1 X X X X X X X X	X 1 X 1 X 1 X 1 X 1 X 1	VECTOR GENERATOR RESET						
2000-2FFF	X 0 1 1	1 X X X X A A A A A A	X 0 0 0 0 0 0 0 0	I/O FOR AUX BD						
2200-2FFF	X 0 1 0	0 A A A A A A A A A A	X 0 0 0 0 0 0 0 0	VECTOR RAM (2K)						
2800-2FFF	X 0 1 2	1 A A A A A A A A A A	X 0 0 0 0 0 0 0 0	VECTOR RAM/VECTOR ROM (2K)						
3000-3FFF	X 0 1 1	1 A A A A A A A A A A	X 0 0 0 0 0 0 0 0	VECTOR ROM (4K)						
3200-3FFF	X 1 0 1	1 A A A A A A A A A A	X 0 0 0 0 0 0 0 0	PROGRAM ROM (4K)						
4000-4FFF	X 1 1 1	1 A A A A A A A A A A	X 0 0 0 0 0 0 0 0	PROGRAM ROM (8K)						



REFERENCE DESIGN

HIGHEST	NOT USED
CR 12	
Q 9	
VR 3	
SW 4	
RP 2	
RI 33	
CI 06	

NOTES:

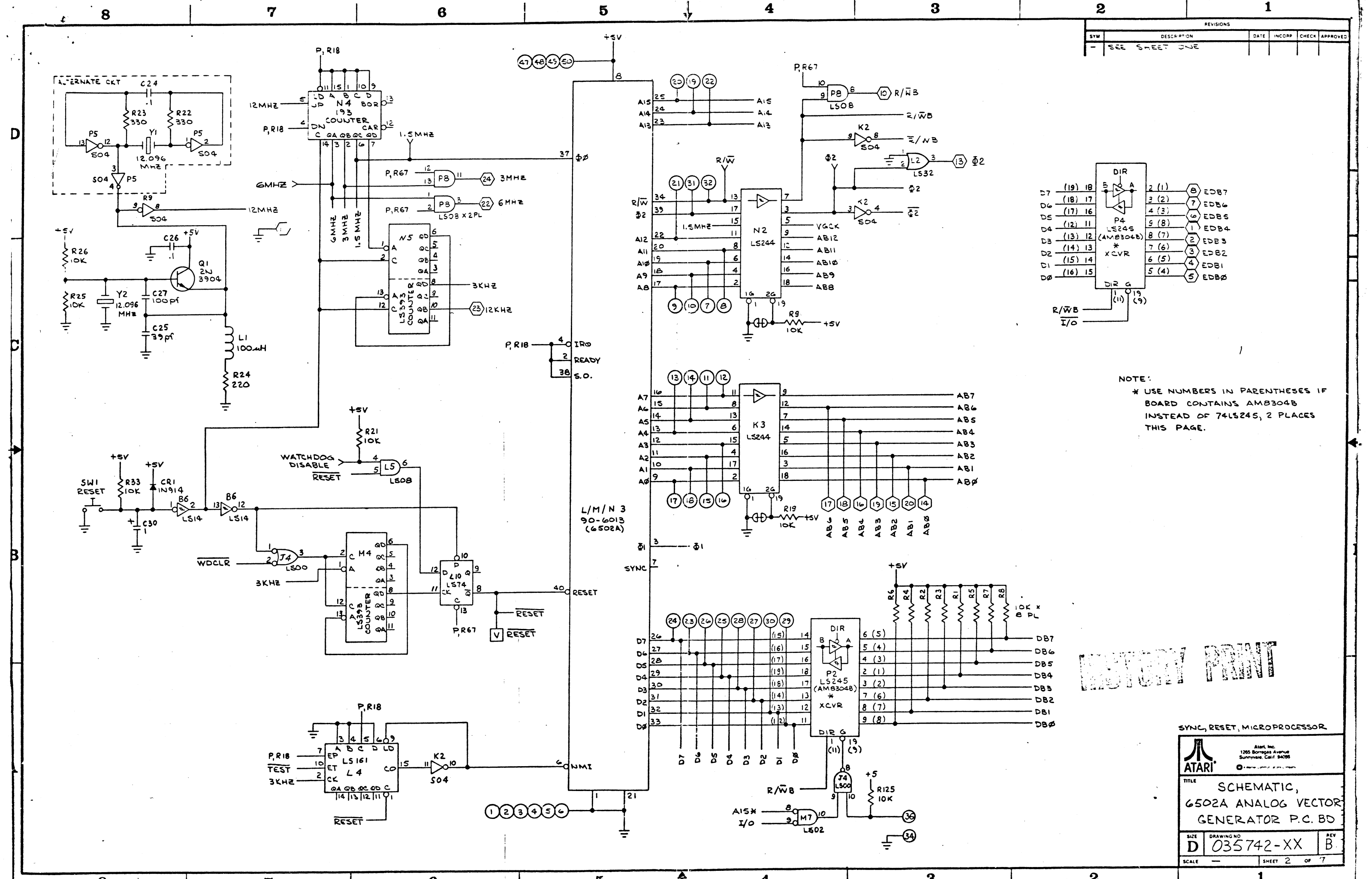
1.  DENOTES J20 CONNECTOR
2.  " TEST "
3.  " J19 "

HISTORY PRIMER

-01 = MC3410 DAC
-02 = AM6012 ADC

A035742		DO NOT SCALE DRAWING		DRAWN BY Z. Tully		DATE 4-1-80	
NEXT ASSY		FIRST USED ON		CHECKED BY		DATE	
OUTSTANDING ECNS				UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON:			
				ANGLES ± 1° ± 0.005 ± 0.005 SURFACE FINISH 125-0			
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				FINISH:			
				PROJECT ENGR		DATE 7-15-80	
				MFG ENGR		DATE	
				 Atari, Inc. 1265 Borregas Avenue Sunnyvale, Calif. 94088 © 1979 of Communications Company			
				TITLE			
				SCHEMATIC,			
				6502A ANALOG VECTOR			
				GENERATOR P. C. BD.			
				SIZE		DRAWING NO	
				D		035742-XX	
				REV		B	
				SCALE		SHEET 1 OF 7	

REVISIONS				
SYM	DESCRIPTION	DATE	INCRP	CHECK
-	SEE SHEET ONE			

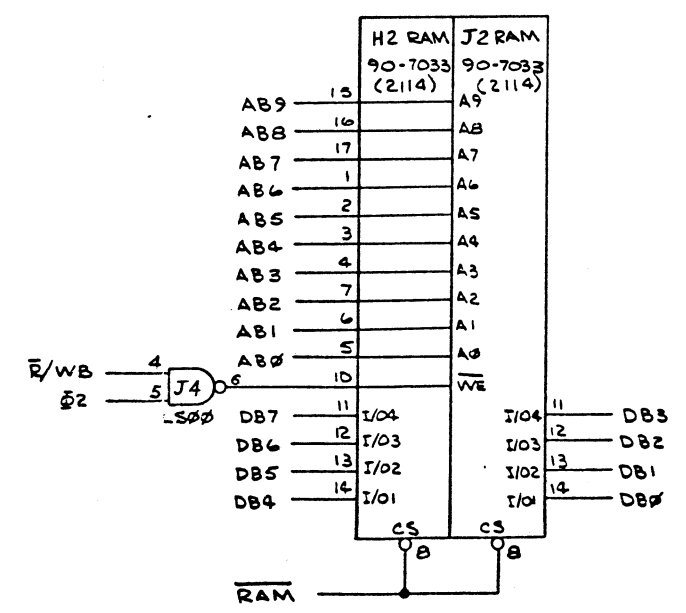
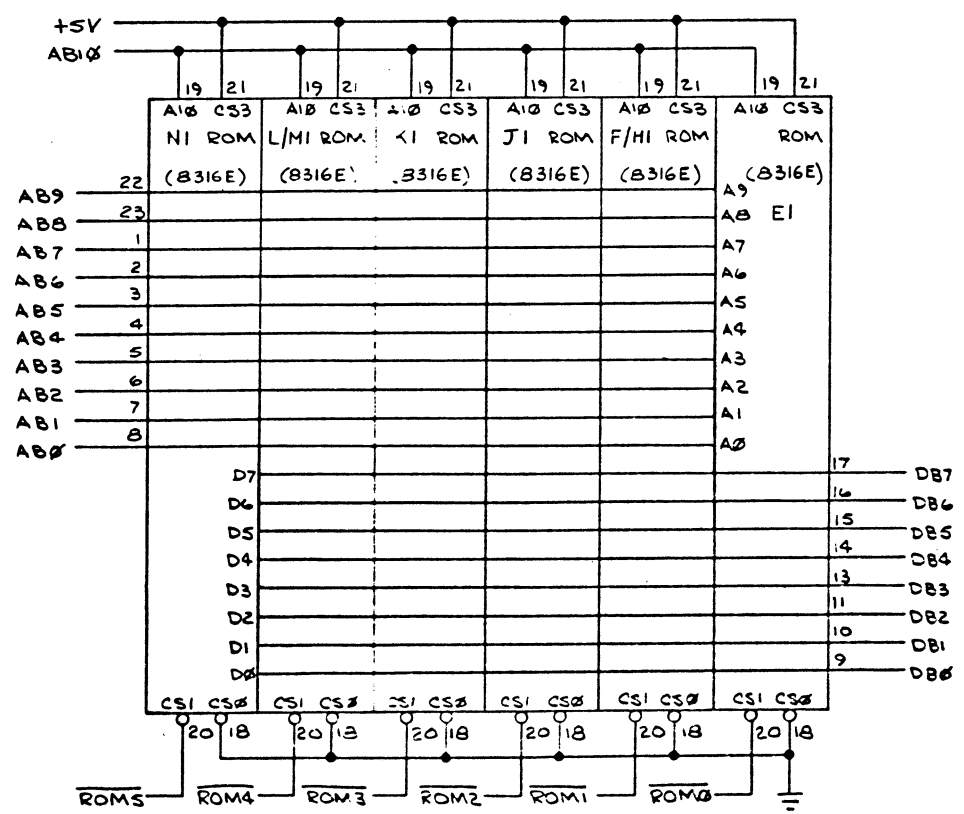
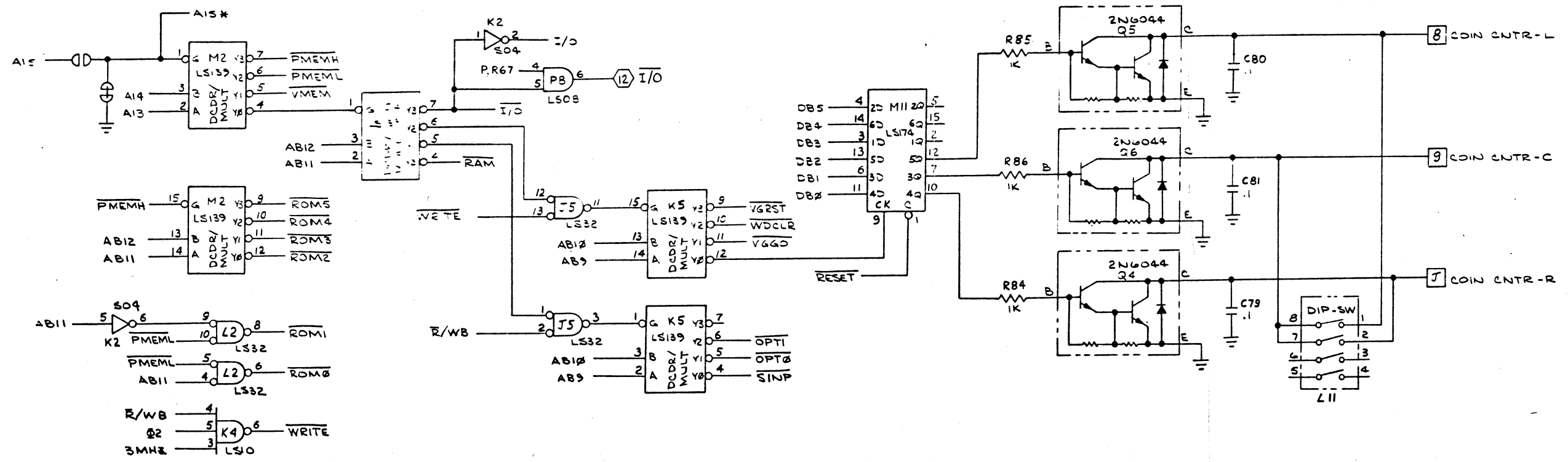


HISTORY PRINT

SYNC, RESET, MICROPROCESSOR

TITLE SCHEMATIC, 6502A ANALOG VECTOR GENERATOR P.C. BD	
SIZE D	DRAWING NO. 035742-XX
SCALE -	SHEET 2 OF 7

REVISIONS					
SYM	DESCRIPTION	DATE	INCRP	CHECK	APPROVED
-	SEE SHEET ONE				



PROTECTED BY PATENT

DECODE, COIN DRIVER, MEMORY

Atari, Inc.
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Sunnyvale, Calif. 94086

TITLE
**SCHEMATIC,
6502A ANALOG VECTOR
GENERATOR P.C. BD.**

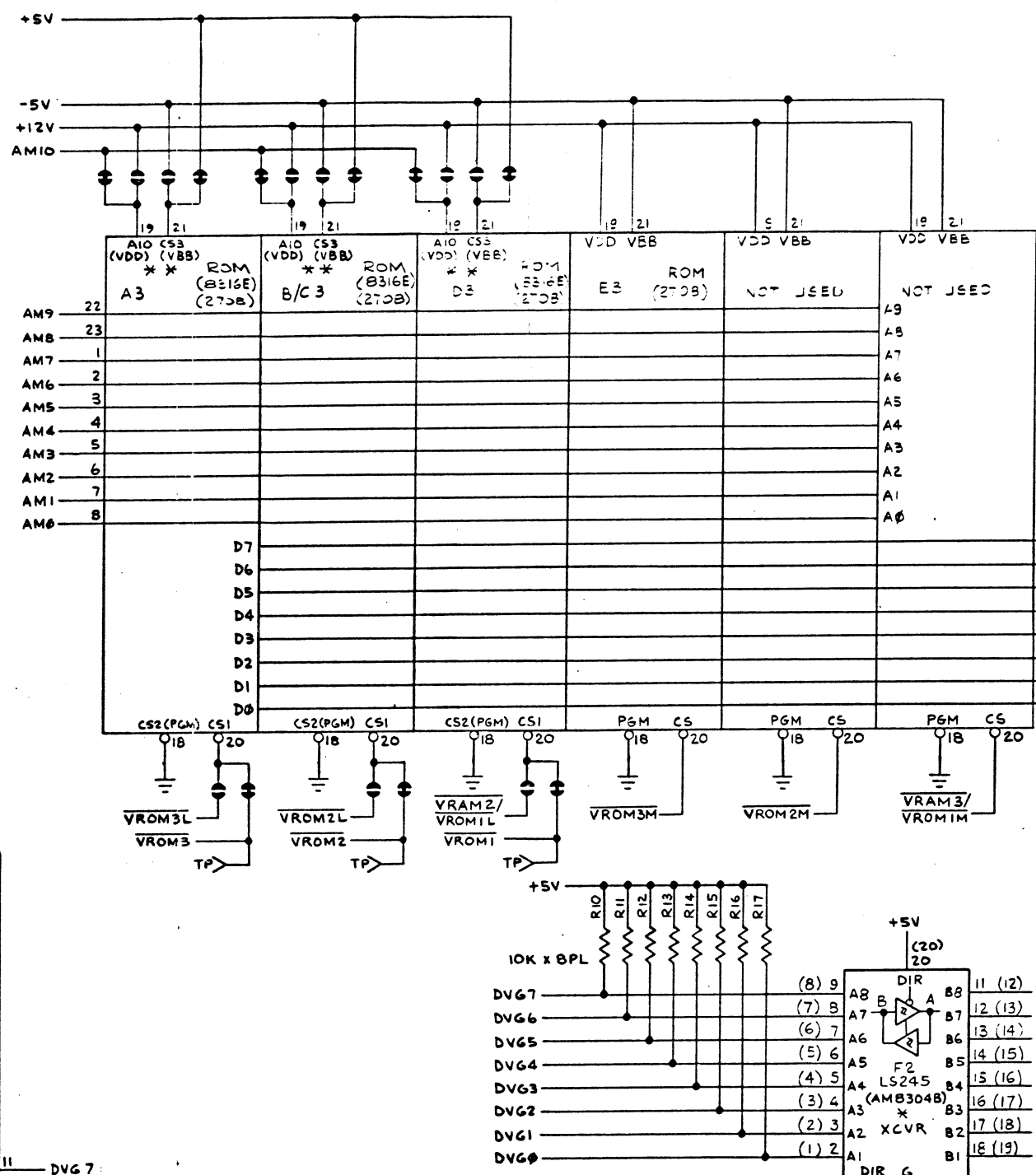
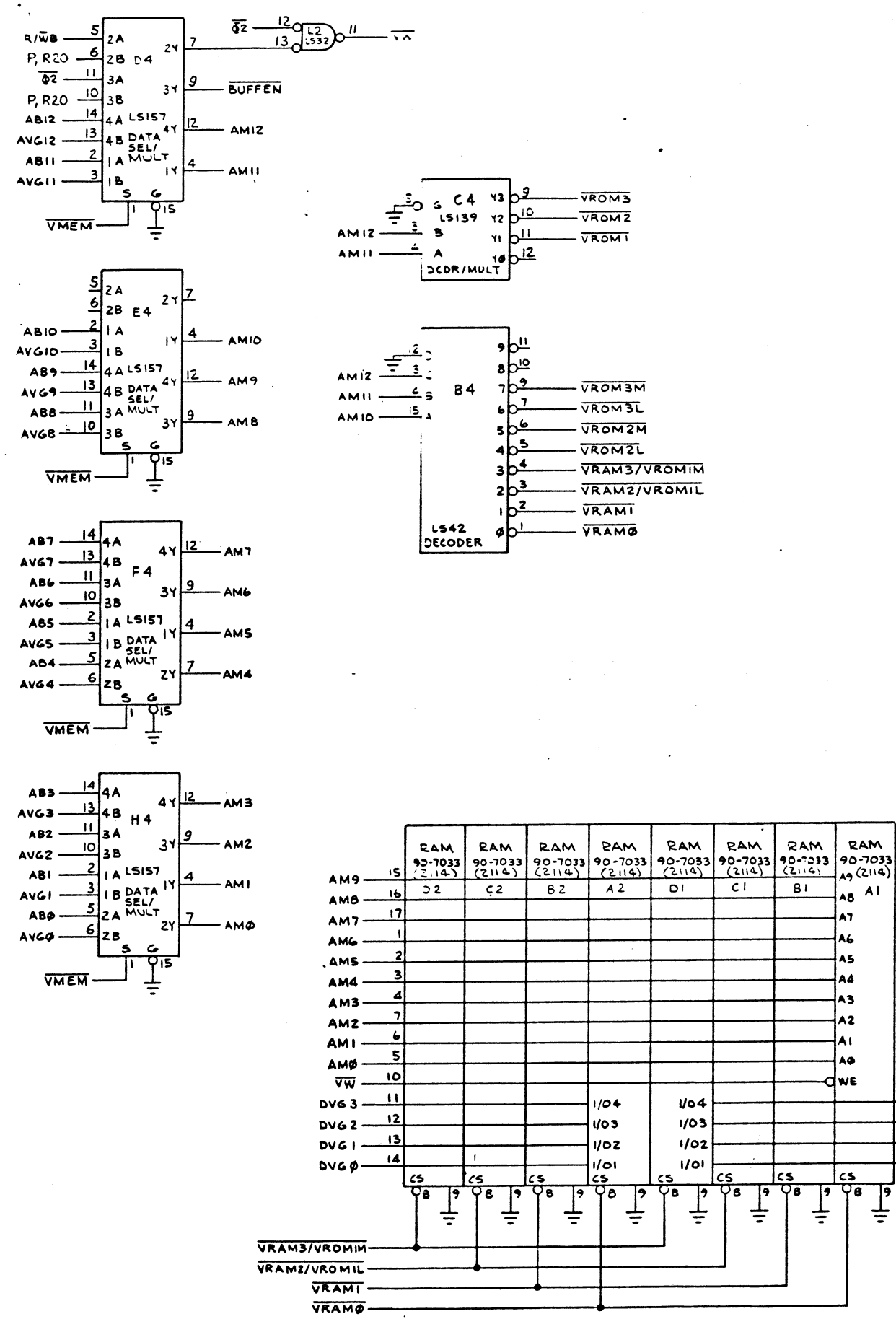
SIZE
D

DRAWING NO.
035742-XX

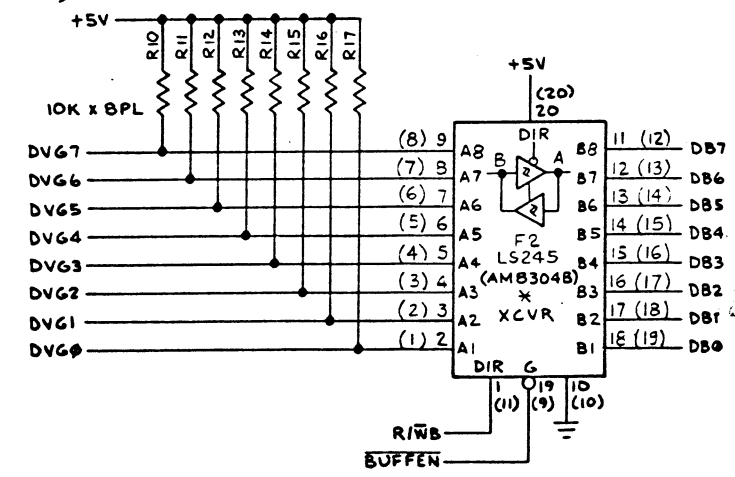
REV
B

SCALE
—

SHEET
3 OF **7**

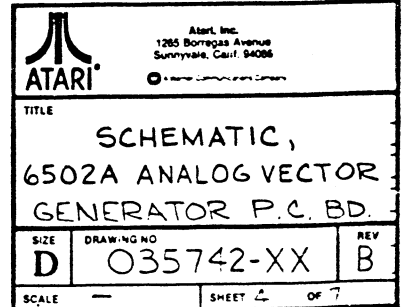


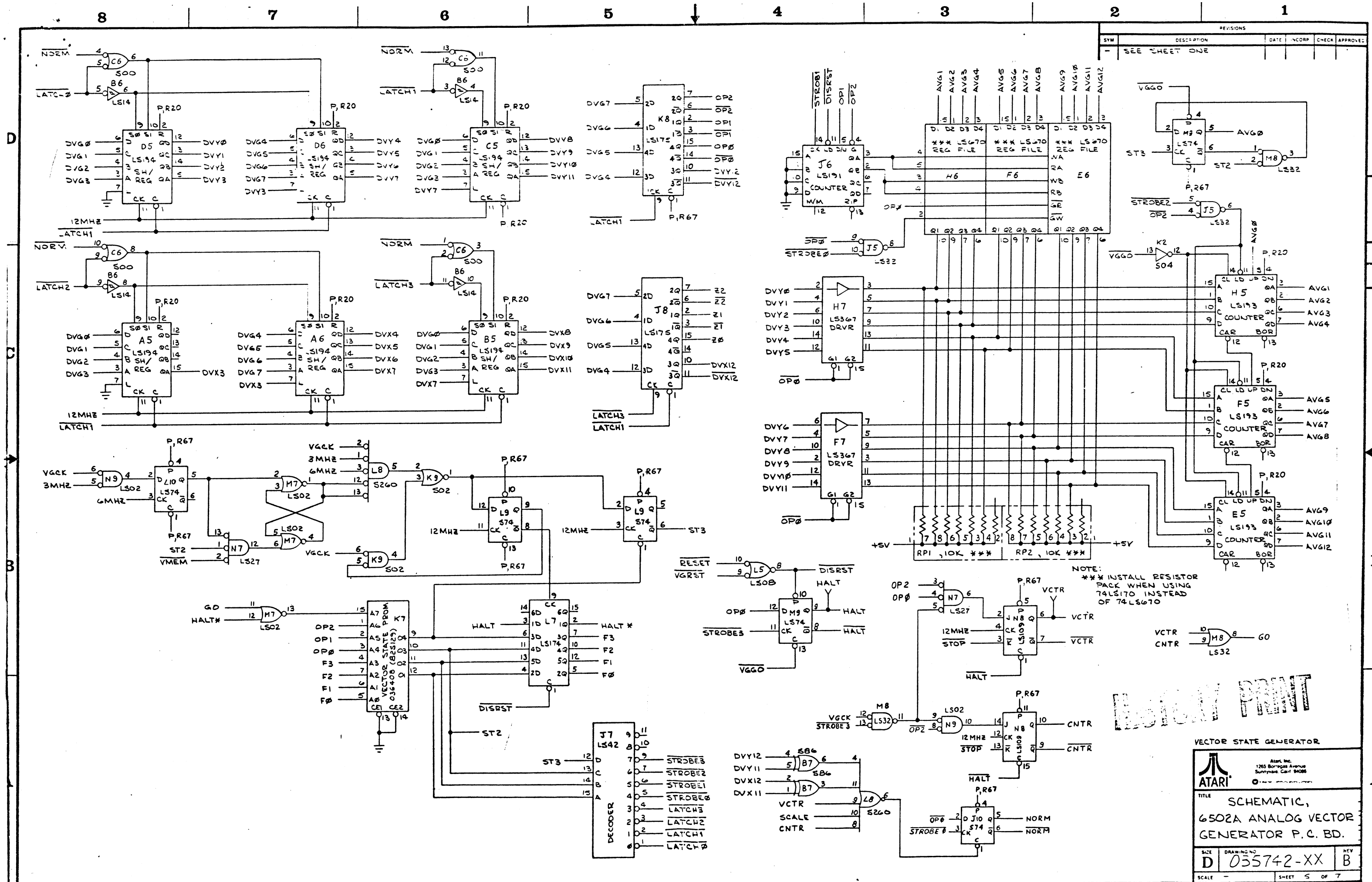
NOTE:
 ** SPLIT PAD CONNECTIONS APPLICABLE TO USE
 OF 8316E 'S. USE OF 2708'S REQUIRE
 BRIDGING OF OPEN PADS AND CUTTING OF
 CONNECTED PADS, 3 PLACES, THIS
 PAGE



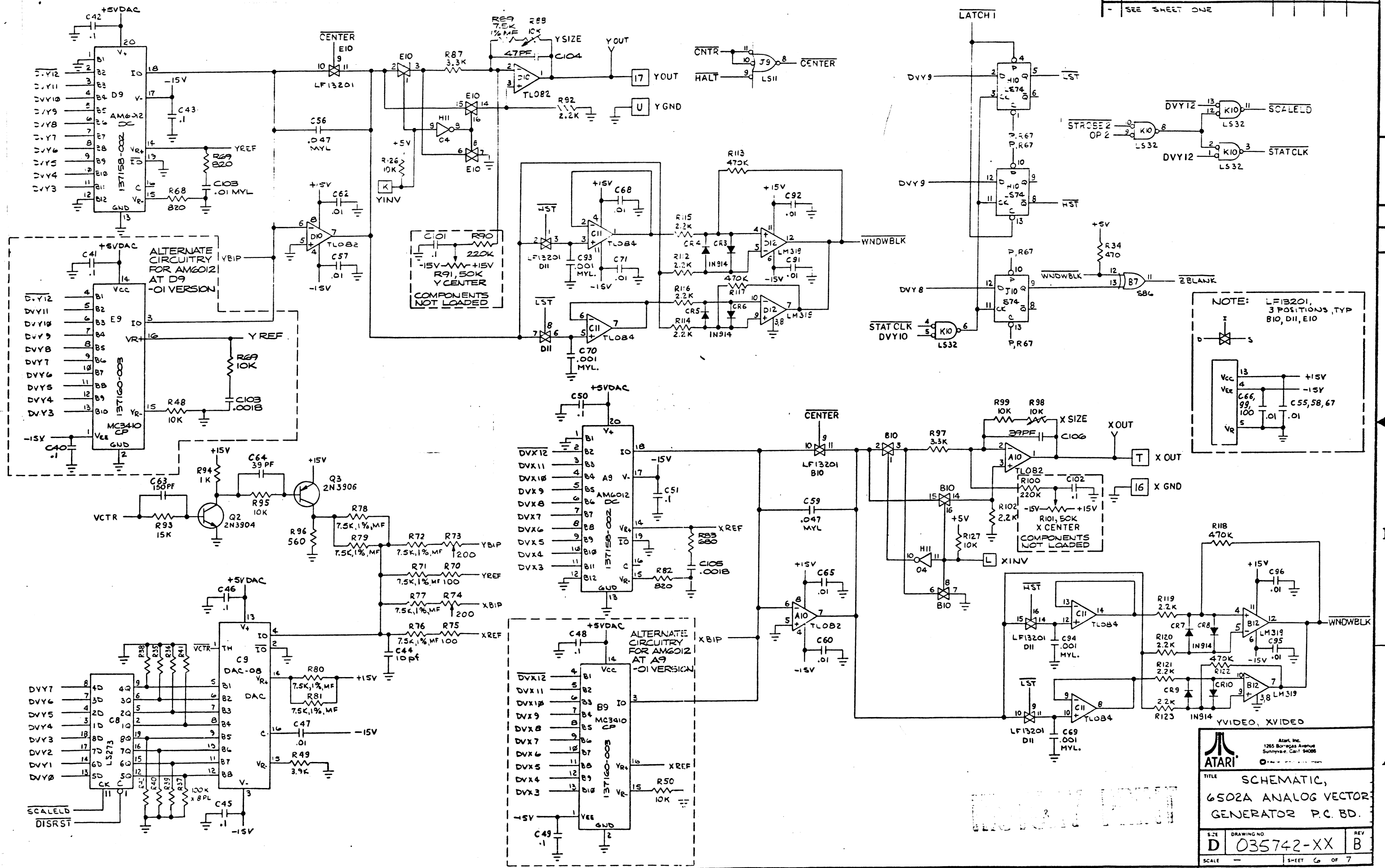
NOTE: * USE NUMBERS IN PARENTHESES IF BOARD CONTAINS AM83048 INSTEAD OF 74LS245

VECTOR MUX, VECTOR MEM

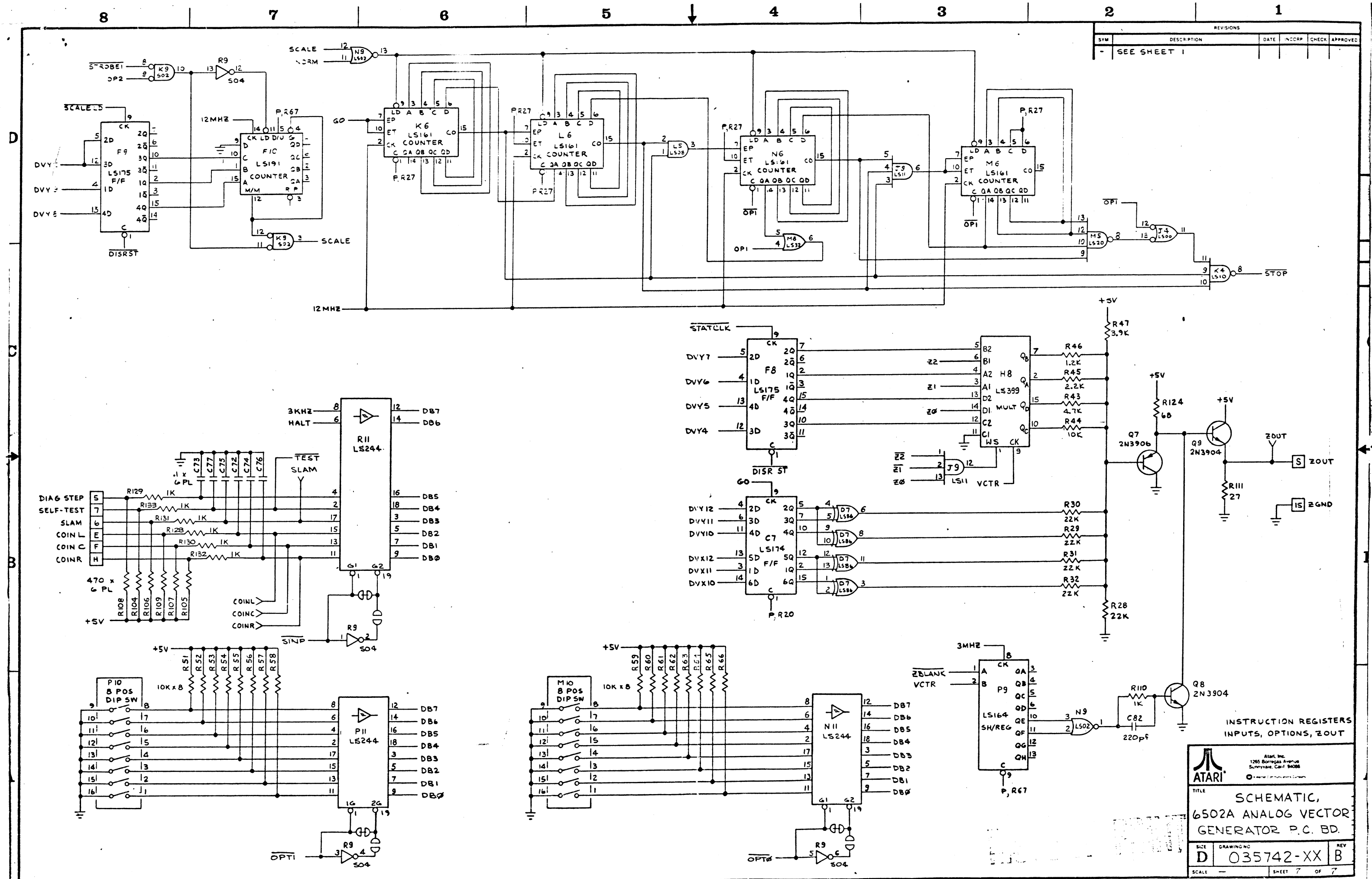




REVISIONS					
SYM	DESCRIPTION	DATE	INCORP	CHECK	APPROVED
-	SEE SHEET ONE				



REVISIONS					
SYM	DESCRIPTION	DATE	INCRP	CHECK	APPROVED
-	SEE SHEET 1				



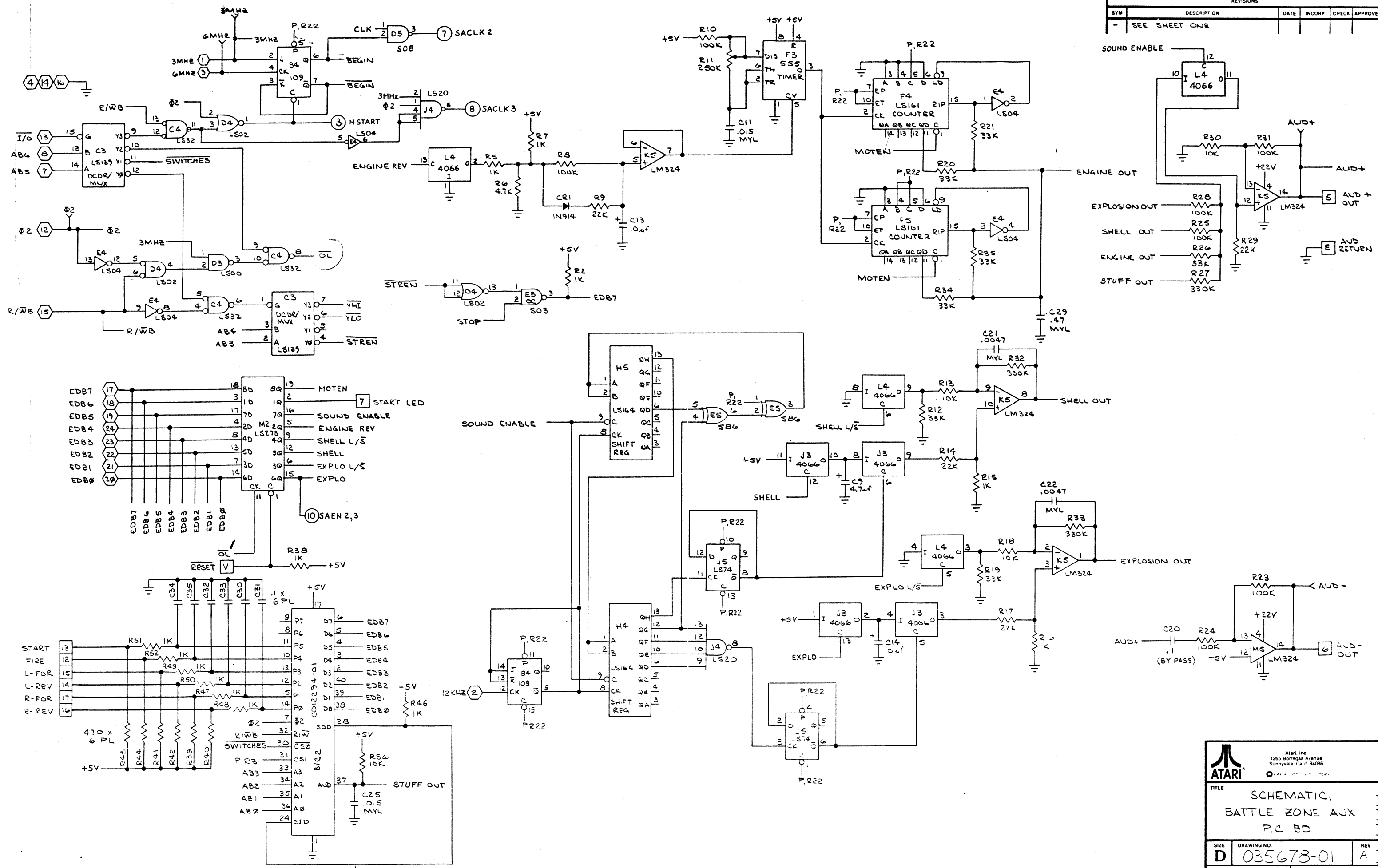

 Atari, Inc.
 1205 Borregas Avenue
 Sunnyvale, Calif. 94088

TITLE
**SCHEMATIC,
 6502A ANALOG VECTOR
 GENERATOR P.C. BD.**

SIZE D DRAWING NO. 035742-XX REV B

SCALE — SHEET 7 OF 7

REVISIONS					
SYM	DESCRIPTION	DATE	INCRP	CHECK	APPROV
-	SEE SHEET ONE				



Atari, Inc.
1265 Borregas Avenue
Sunnyvale, Calif. 94086

TITLE
**SCHEMATIC,
BATTLE ZONE AUX
P.C. BD.**

SIZE D	DRAWING NO. 035678-01	REV A
SCALE —		SHEET 3 OF 3

REVISIONS				
SYM	DESCRIPTION	DATE	INCP	APPROV
-	SEE SHEET ONE			

D

C

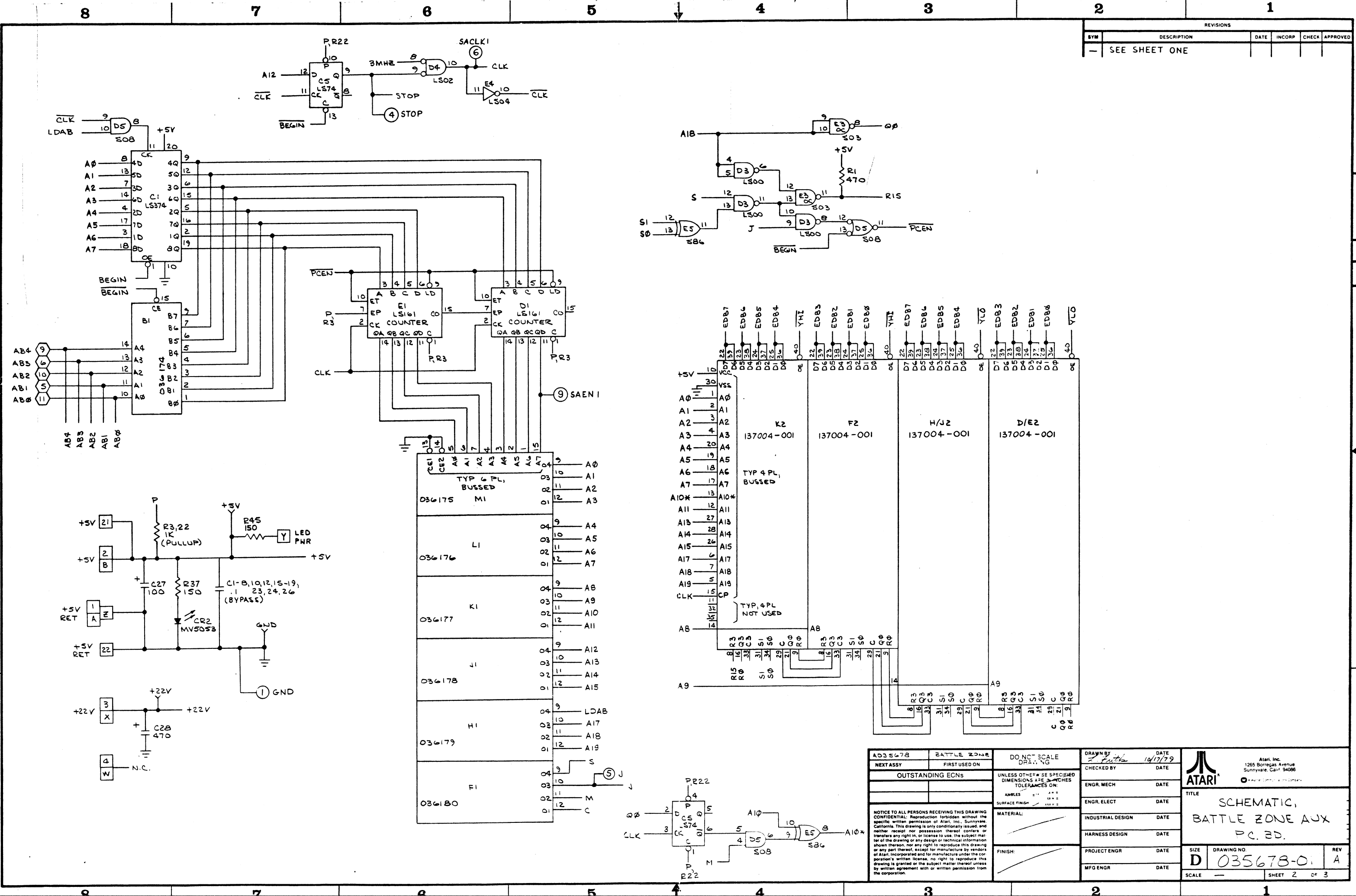
B

A

C

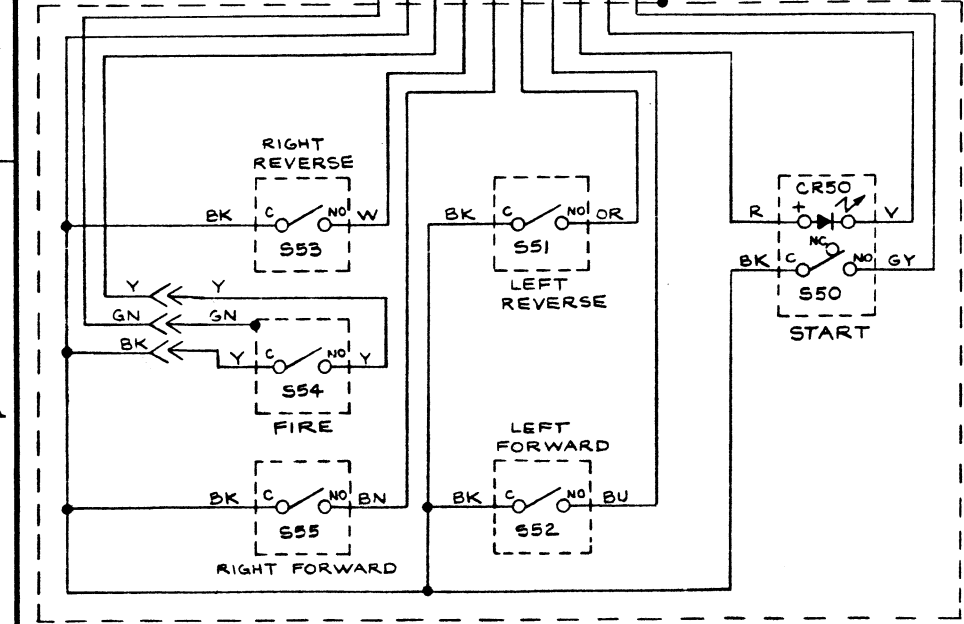
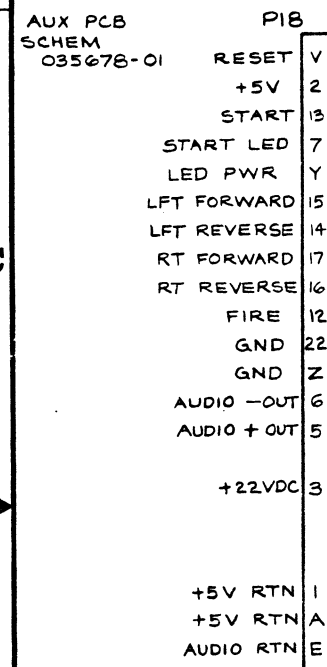
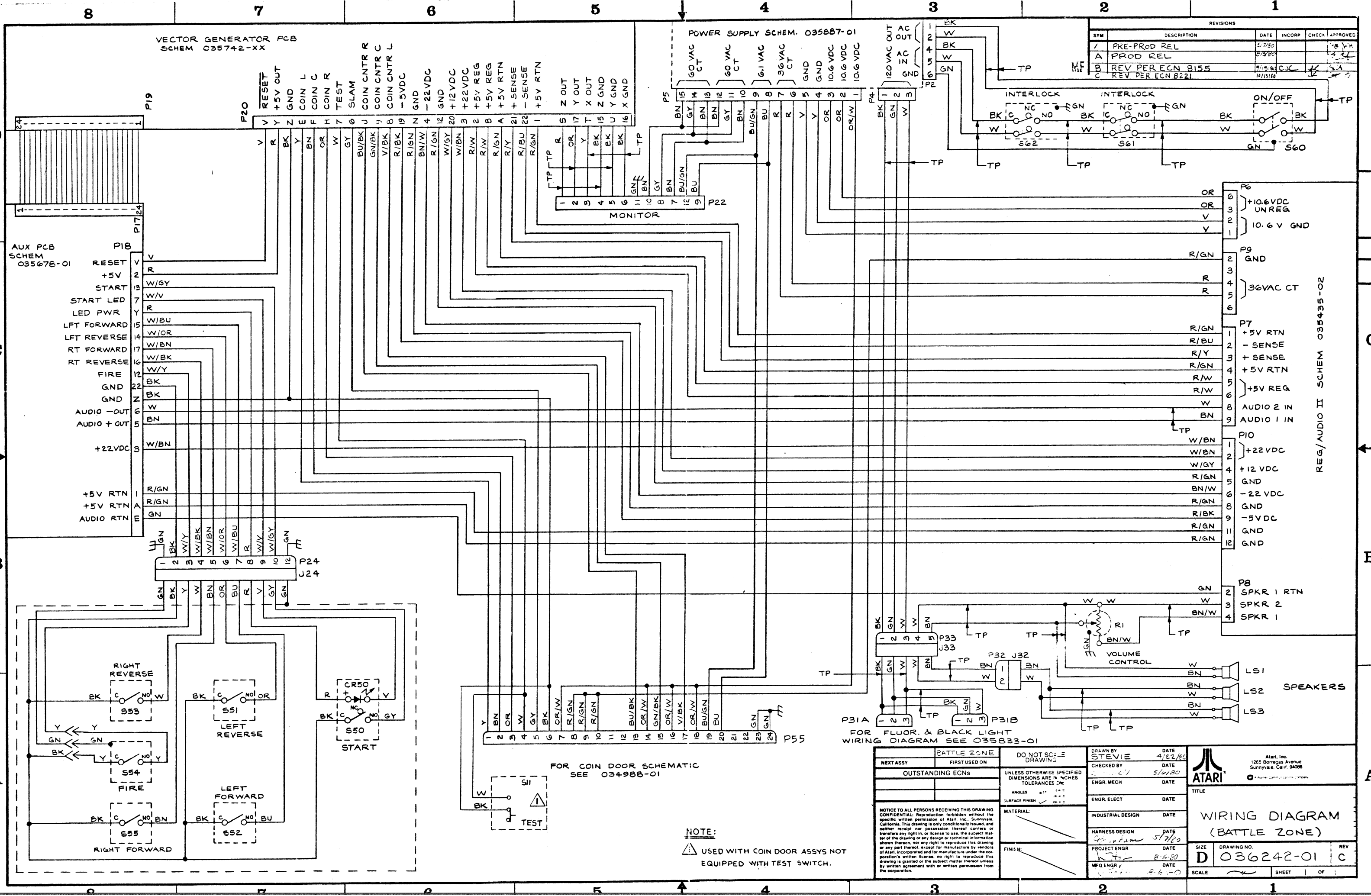
B

A

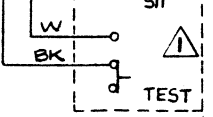


AD35678		BATTLE ZONE		DO NOT SCALE DRAWING		DRAWN BY <i>[Signature]</i> DATE 10/17/79		Atari, Inc. 1265 Borregas Avenue Sunnyvale, Calif. 94086 © 1979 ATARI, INC.	
NEXT ASSY		FIRST USED ON				CHECKED BY <i>[Signature]</i> DATE			
OUTSTANDING ECNS				UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES				TITLE	
				TOLERANCES ON:				SCHEMATIC, BATTLE ZONE AUX PC BD.	
				ANGLES: 30° 45° 60° 90° 120° 135° 150° 180°				SIZE	
				SURFACE FINISH: 100% 200% 300% 400% 500% 600% 700% 800% 900% 1000%				D 035678-01	
				MATERIAL:				REV	
				FINISH:				A	
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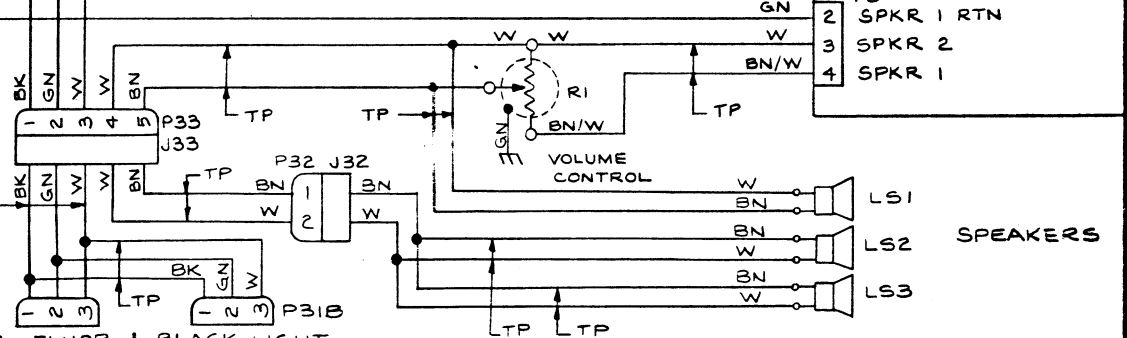
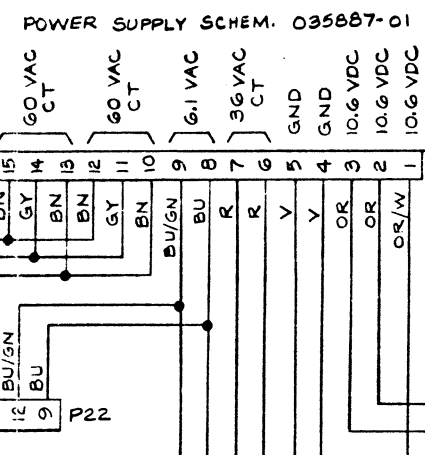
VECTOR GENERATOR PCB
SCHEM 035742-XX



FOR COIN DOOR SCHEMATIC
SEE 034988-01



NOTE:
USED WITH COIN DOOR ASSYS NOT
EQUIPPED WITH TEST SWITCH.



FOR FLUOR. & BLACK LIGHT
WIRING DIAGRAM SEE 035833-01

REVISIONS				
SYM	DESCRIPTION	DATE	INCRP	CHECK
1	PRE-PROD REL	5/7/80		
A	PROD REL	8/3/80		
B	REV PER ECN 8155	9/15/80		
C	REV PER ECN 8221	10/15/80		

BATTLE ZONE		DO NOT SCALE DRAWING		DRAWN BY STEVIE DATE 4/22/80	
NEXT ASSY FIRST USED ON		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE:		CHECKED BY DATE 5/6/80	
OUTSTANDING ECNS		ANGLES: ±1° ±.5° ±.2°		ENGR. MECH DATE	
		SURFACE FINISH: 25 ± 2		ENGR. ELECT DATE	
		MATERIAL:		INDUSTRIAL DESIGN DATE	
		FINISH:		HARNESS DESIGN DATE 5/7/80	
				PROJECT ENGR DATE 8-6-80	
				MFG ENGR DATE 8-6-80	
				TITLE WIRING DIAGRAM (BATTLE ZONE)	
				SIZE D	
				DRAWING NO. 036242-01	
				REV C	
				SCALE	
				SHEET 1 OF 1	