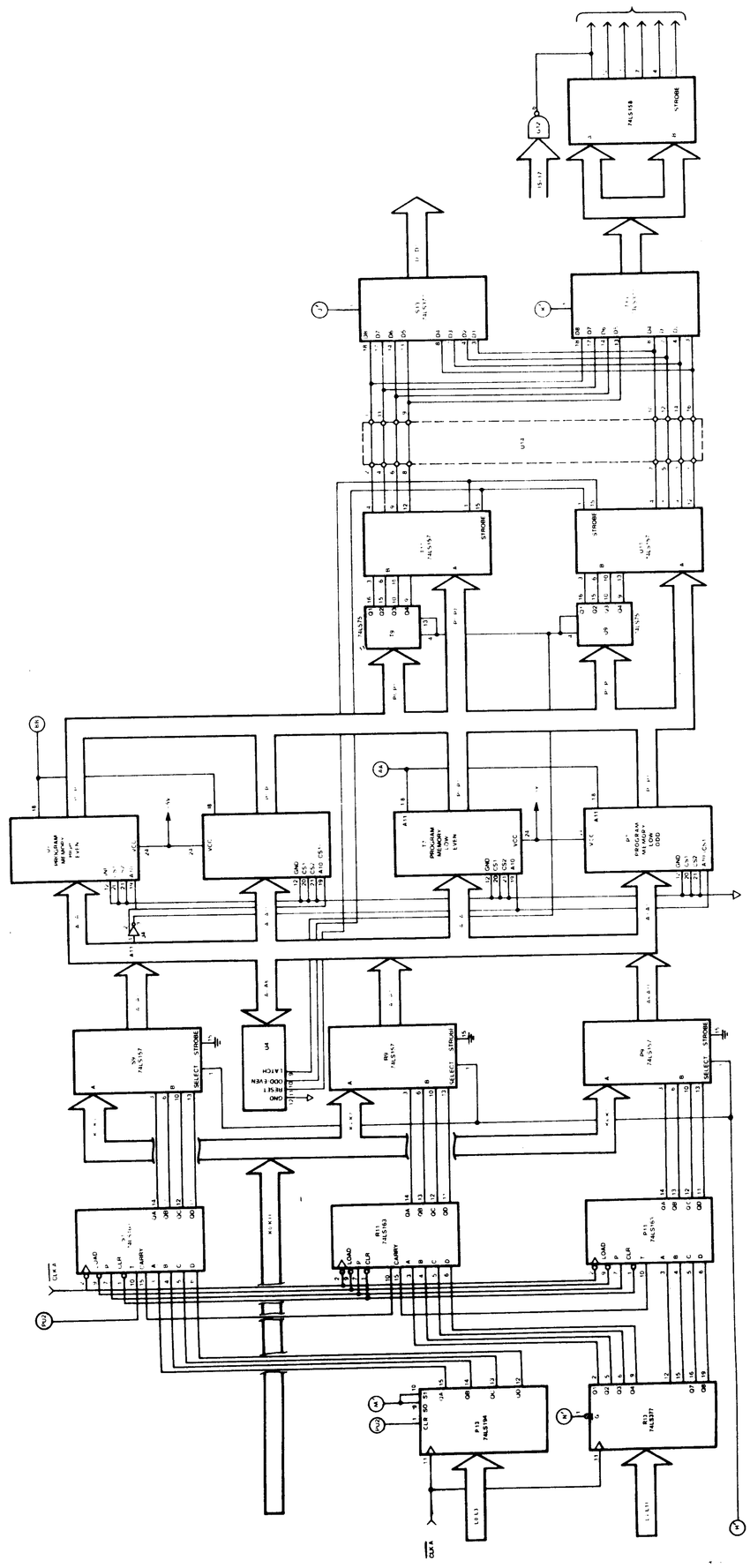

Figure A-6 Program Memory, Data Bus Multiplexing
Instruction and Data Register